

Mark Narveson

Of Counsel

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About Mark

Mark Narveson has been a patent attorney for nearly 15 years, focusing on patent preparation and prosecution across technical disciplines, including 3D computer graphics, computer animation, virtual computing environments, virtual reality, big data systems and electronic financial trading platforms. He couples technical expertise with business and industry acumen to advise clients at all stages of innovation.

Prior to entering private practice, Mark spent more than 30 years in high-tech industries, holding roles from development engineering and project management to product marketing. He has worked for many well-known names, including Sun Microsystems, GTE Government Systems, The Grass Valley Group and Calma. His work included developing and supporting products related to computer-aided design (CAD) systems, 3D computer graphics, digital signal processing and analysis, media production and video editing.

During law school, Mark served as executive chair of the McGeorge Moot Court Honor Society, as an associate managing editor of the *Pacific McGeorge Global Business & Development Law Journal* and as a judicial extern for the Honorable Robert C. Jones of the US District of Nevada.

Credentials

Education

- Pacific McGeorge School of Law, J.D., 2011
- University of California, Davis, M.B.A., 1999
- Santa Clara University, M.S.C.E, 1991
- DeVry University, B.S.E.E.T, 1981

Admissions

- California, 2011
- U.S. Patent and Trademark Office, 2009

Courts

• Supreme Court of California

Memberships & Affiliations

• Society of Motion Picture and Television Engineers, Prior Member of Board of Governors

Expertise

Services

• Intellectual Property & Technology

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